

<b>Notice of References Cited</b>		Application/Control No.	Applicant(s)/Patent Under Reexamination	
		10/039,289	WOLRICH ET AL.	
		Examiner	Art Unit	Page 1 of 1
		Srirama Channavajjala	2166	

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,779,084	08-2004	Wolrich et al.	711/118
	B	US-6,426,957	07-2002	Hauser et al.	370/413
	C	US-2004/0179533	09-2004	Donovan, Peter	370/395.5
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Mathew Adiletta et al. "the next generation of Intel IXP Network processors", Intel technology journal, network processors, vol 6, issue:03, published august 15, 2002, pp 6-18,
	V	G.Kornaros, et al. "A fully-programmable memory management system optimizing queue handling at multi Gigabit rates", ACM, June 2-6, 2003, pp 54-59
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.